

FIG. 1

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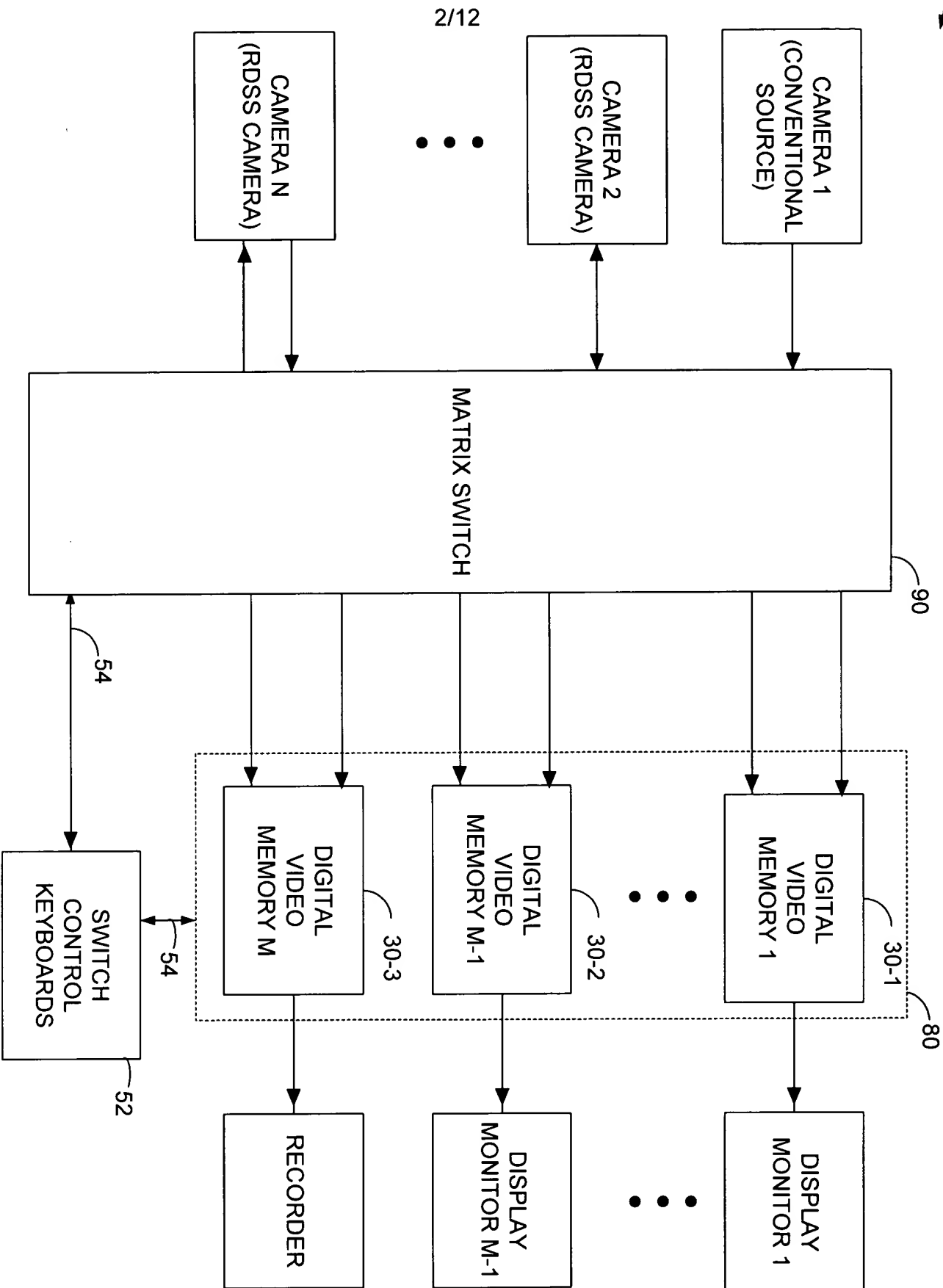
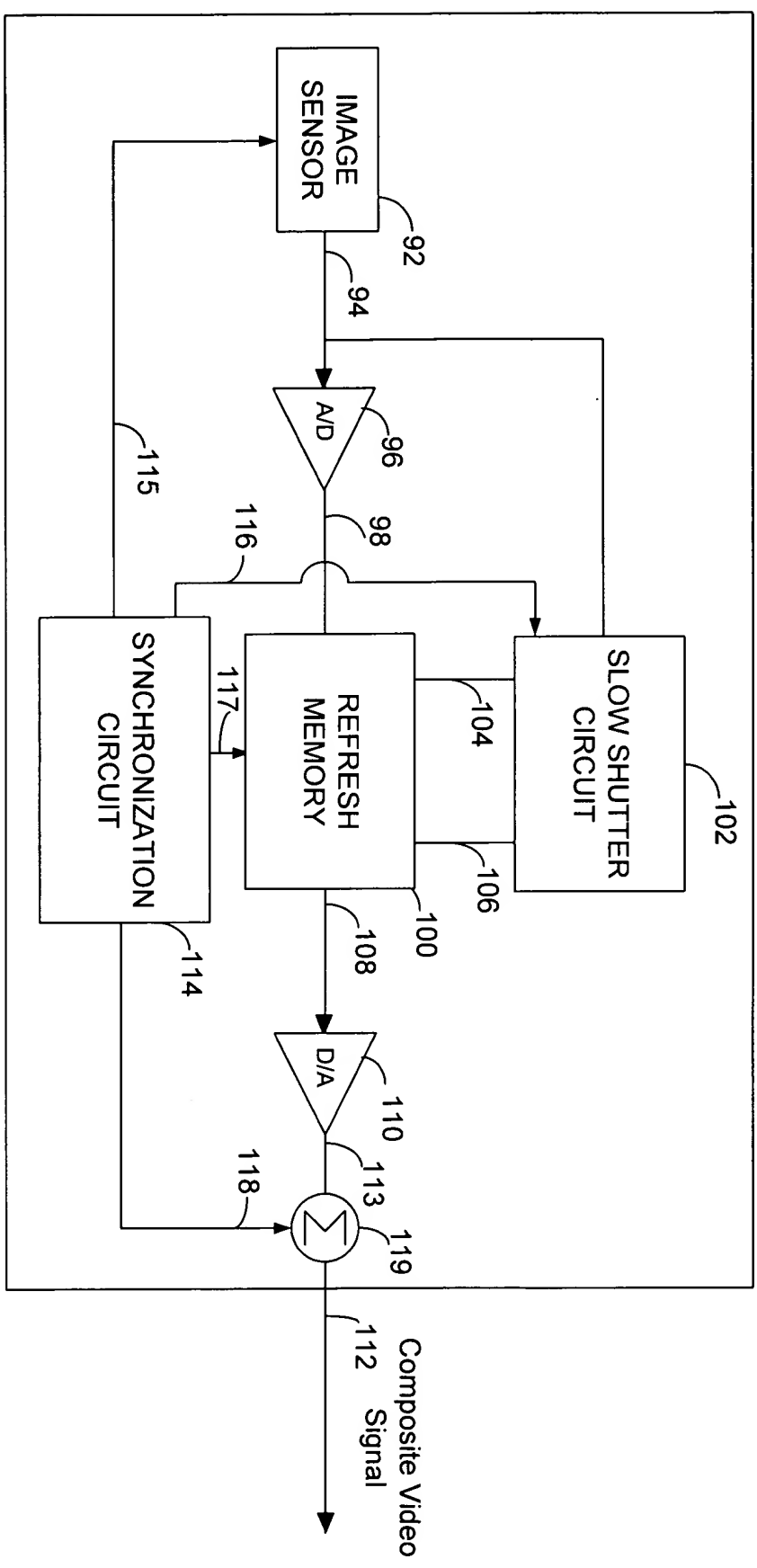


FIG. 2

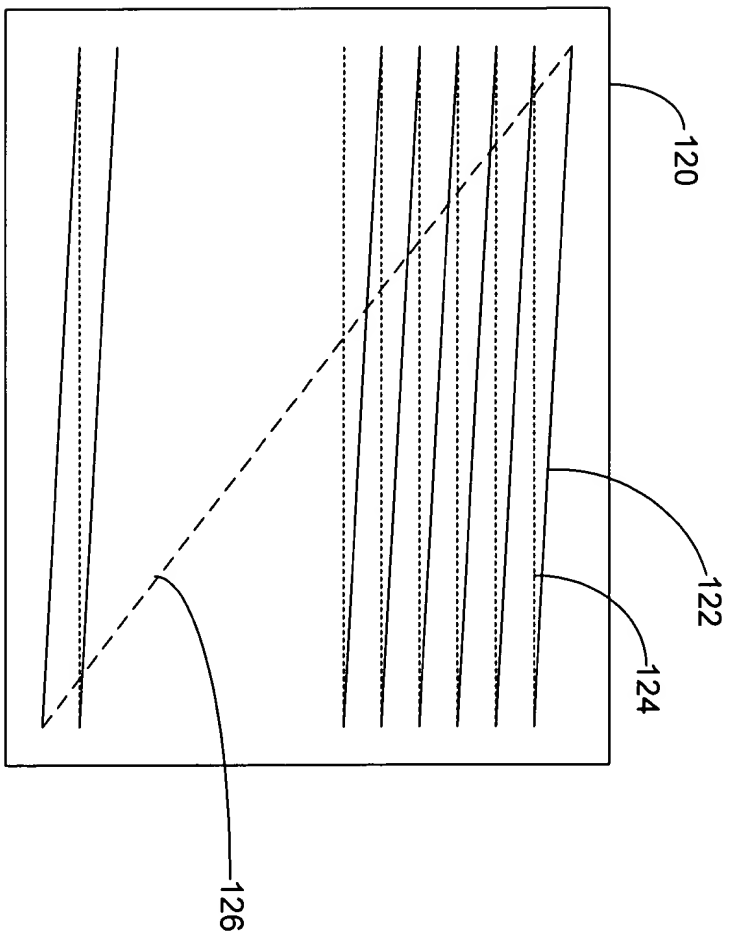
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CAMERA  
**FIG. 3**

PRIOR ART

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**FIG. 4**

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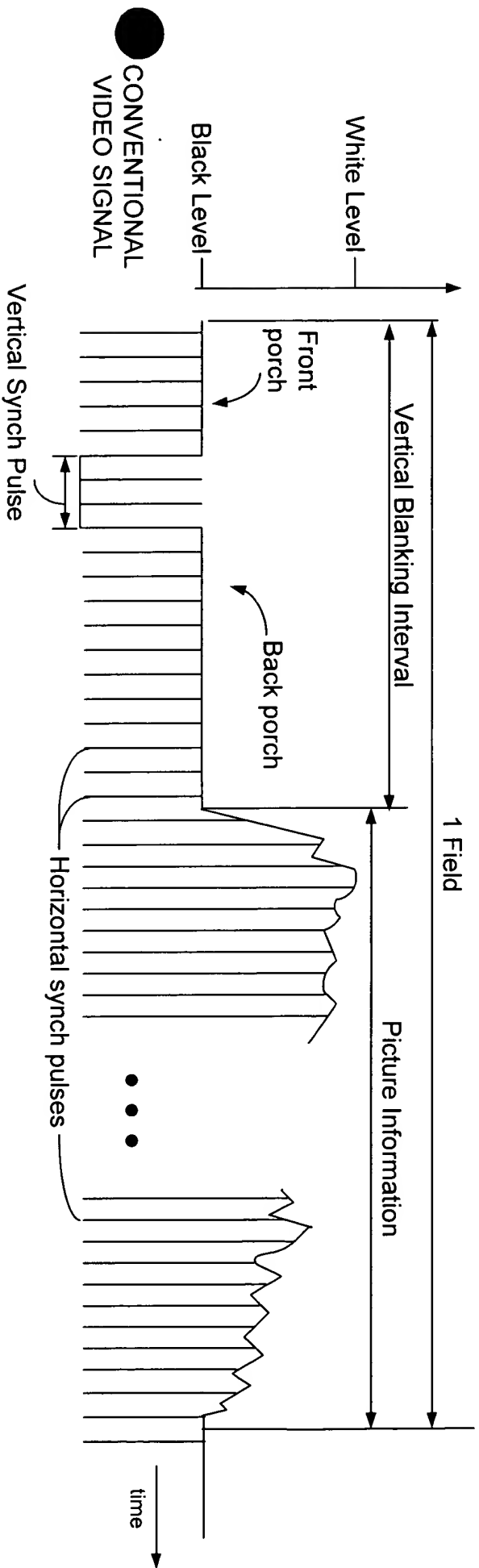
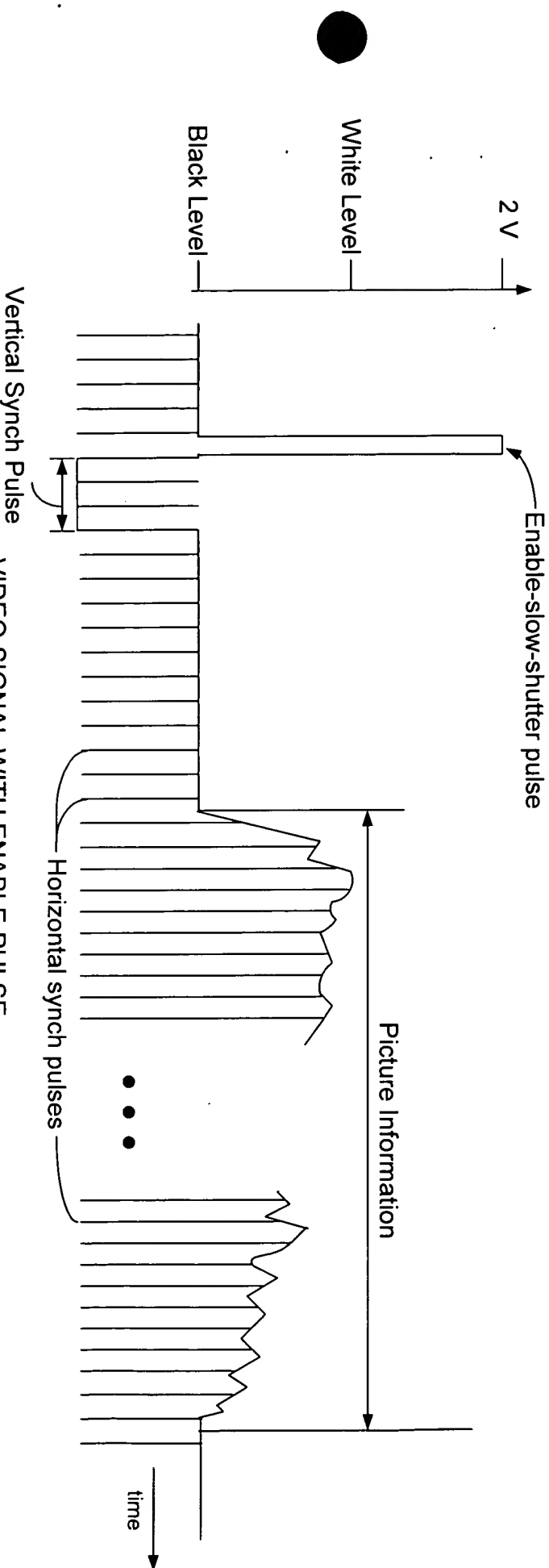
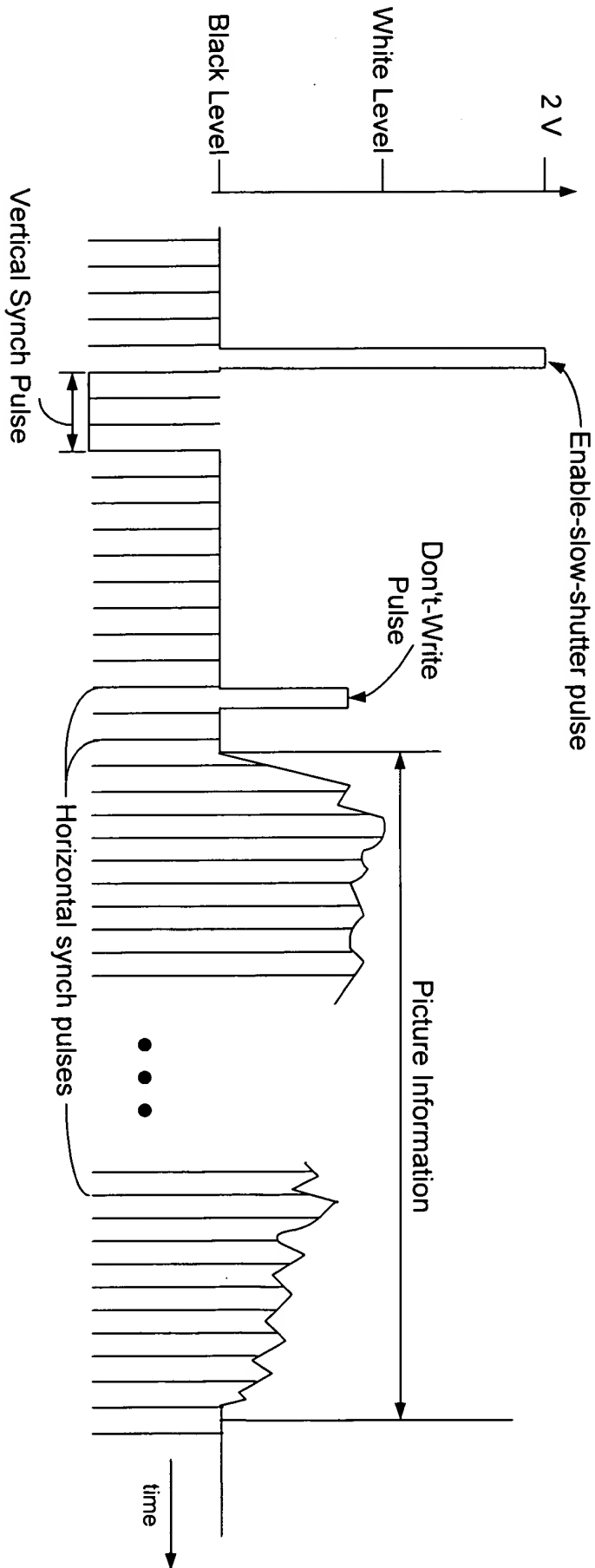


FIG. 5A



VIDEO SIGNAL WITH ENABLE PULSE

FIG. 5B



VIDEO SIGNAL WITH DONT-WRITE PULSE  
**FIG. 5C**

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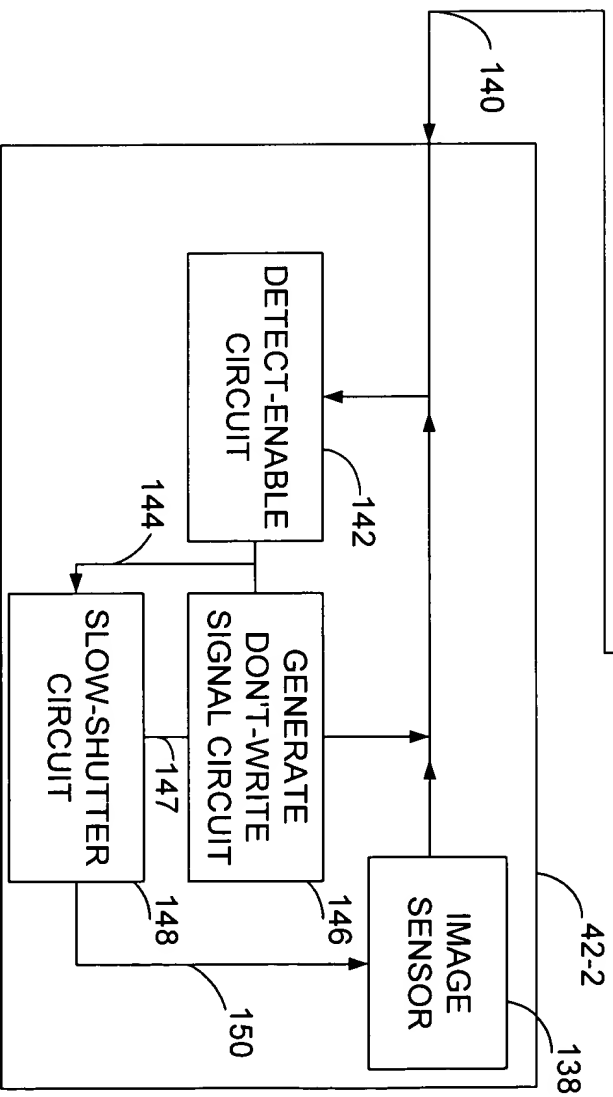
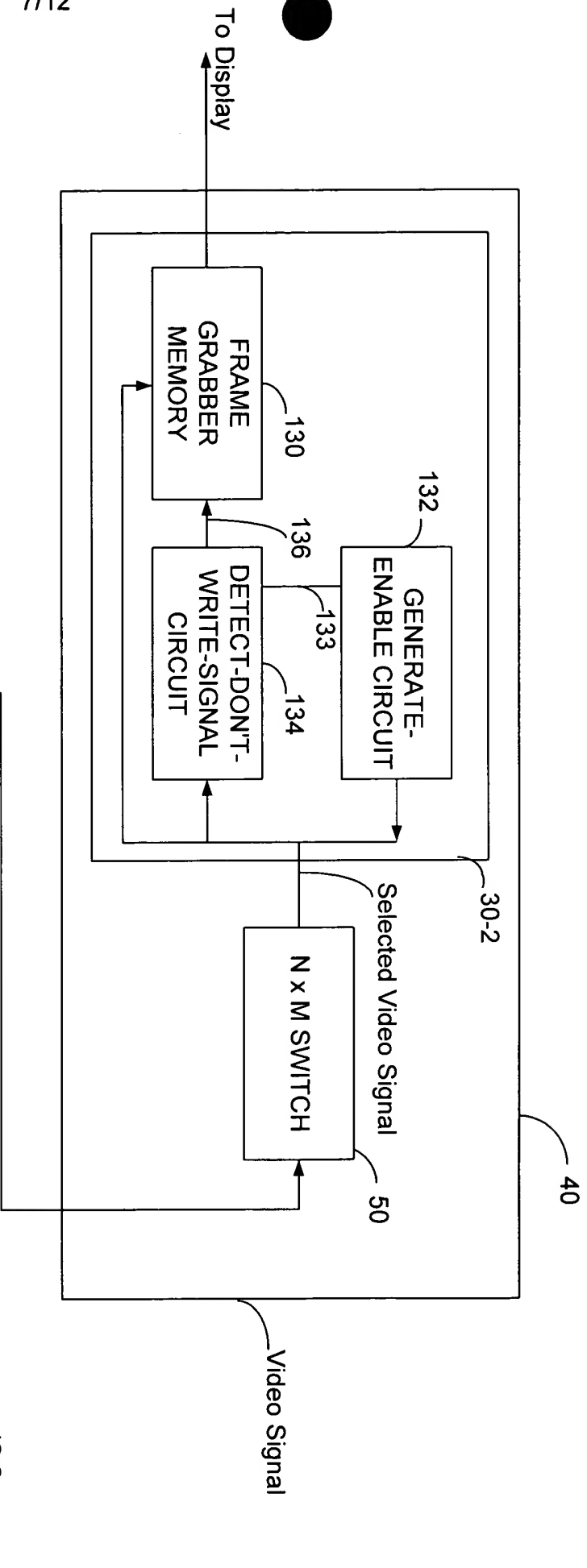


FIG. 6

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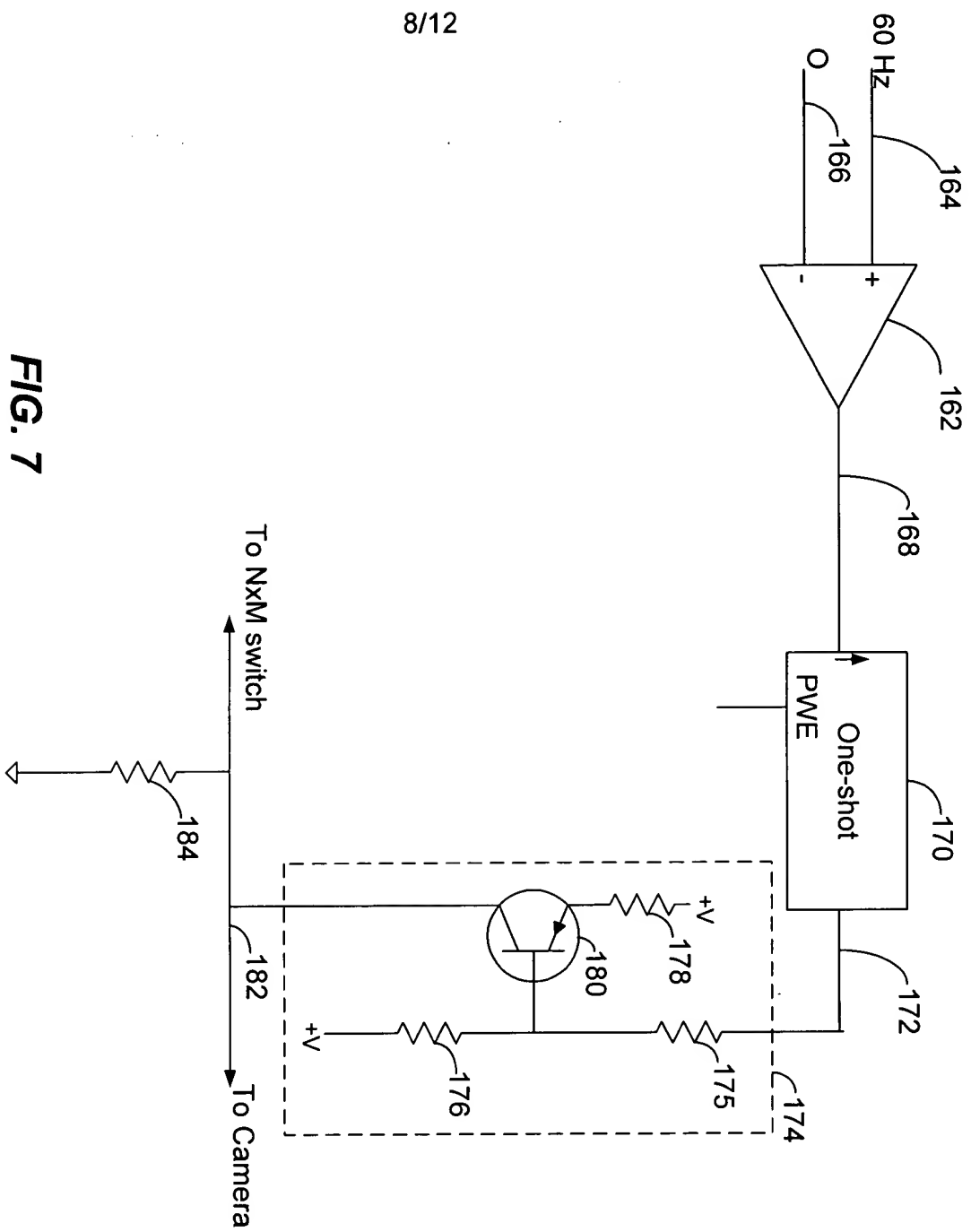
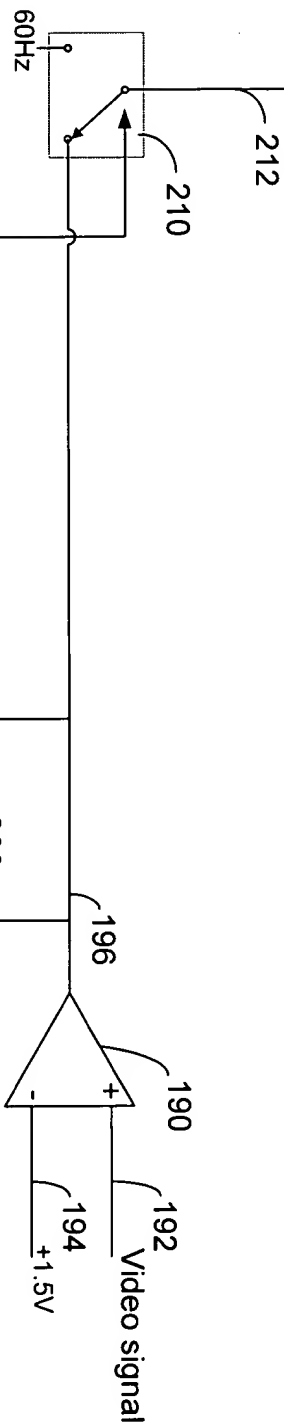


FIG. 7



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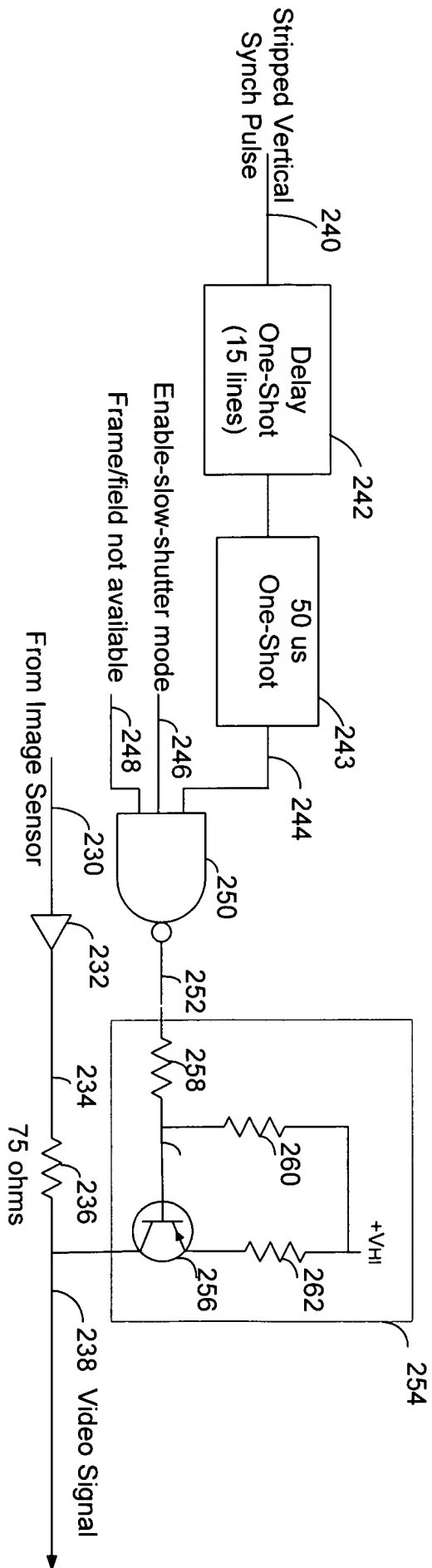
CAMERA  
SYNCHRONIZER LOCK  
CIRCUITS



CAMERA

FIG. 8

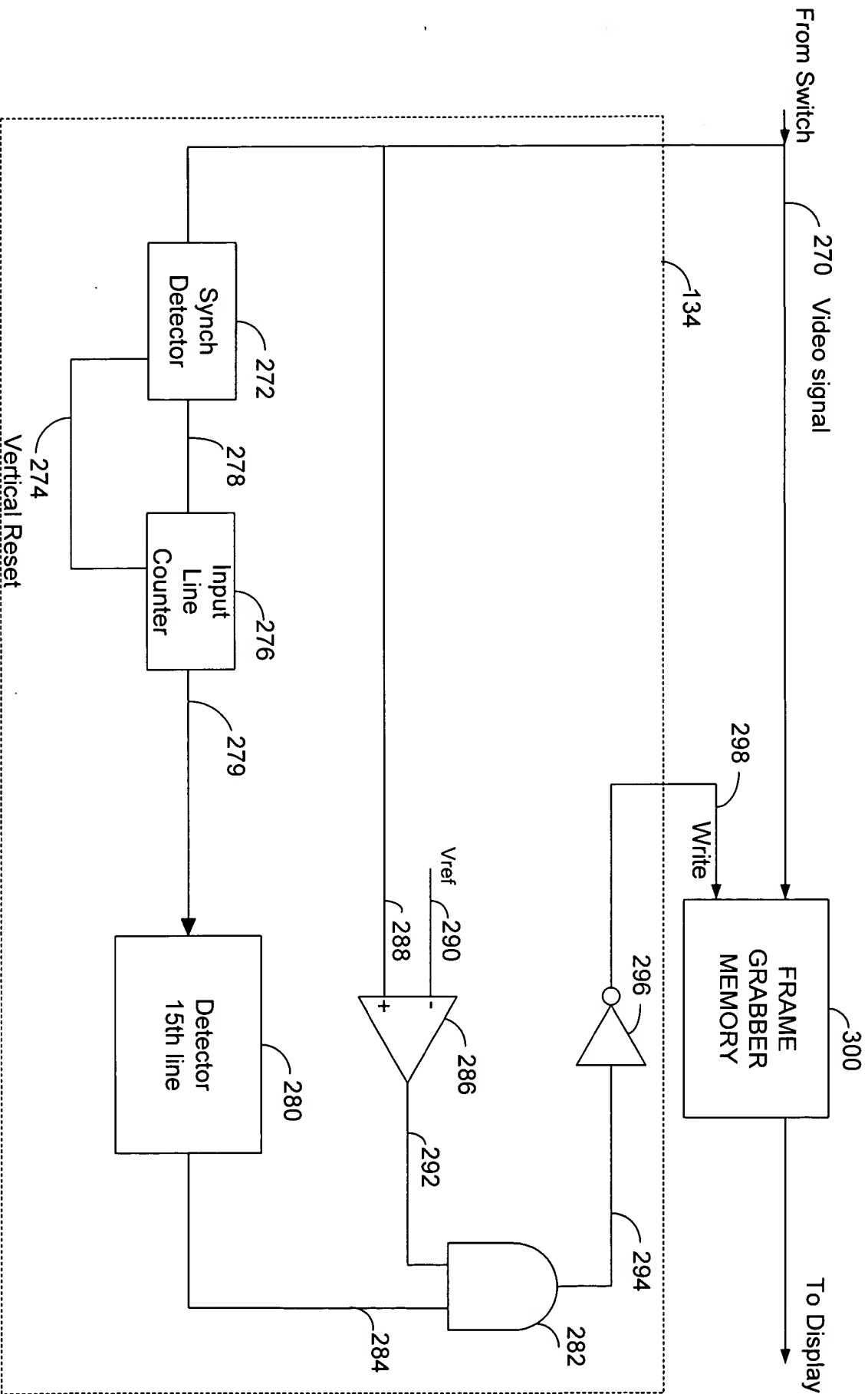
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GENERATE DON'T WRITE SIGNAL CIRCUIT

FIG. 9

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DIGITAL VIDEO MEMORY WITH THE DETECT-DON'T-WRITE SIGNAL CIRCUIT

**FIG. 10**

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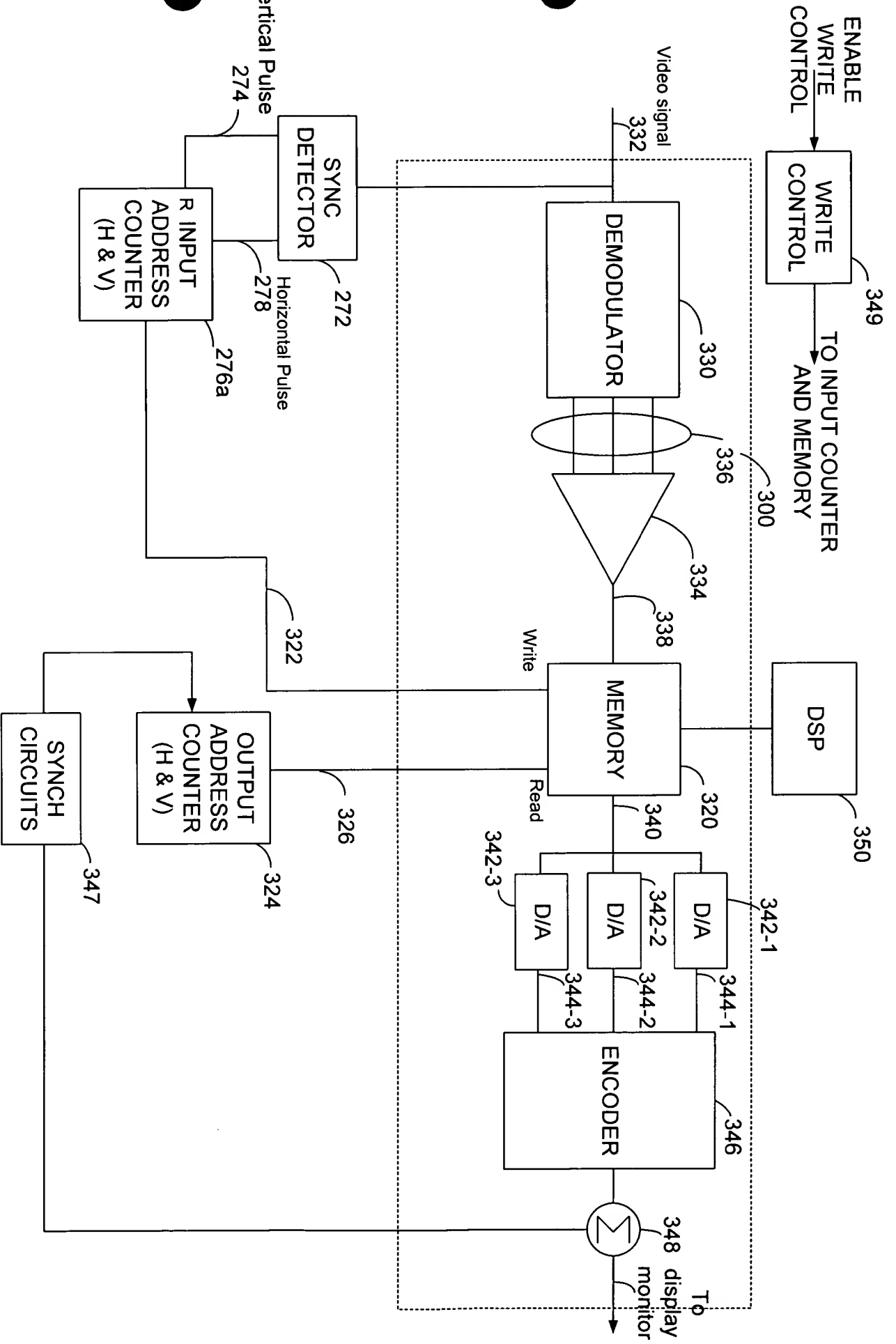


FIG. 11

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